

XC5VLX50-1FFG676I Xilinx Product Details PDF





Images are for reference See Product Specification













Part Number: XC5VLX50-1FFG676I

BomEasy Part: XC5VLX50-1FFG676I-2540731

Category: <u>FPGA(Field-Programmable Gate Array)</u>

Manufacturer: Xilinx

Applications: ■Automotive ■Enterprise systems ■Personal electronics

Description: FPGA Virtex-5 LX Family 46080 Cells 65nm (CMOS)

Technology 1V 676-Pin FCBGA

RoHS: Lead free / RoHS Compliant

Stock Category: In stock

Franchised Distributor **Stock Resource:**

Warranty: 1 Year BomEasy Guarantee <u>» Learn More</u>

EDA/CAD Models: XC5VLX50-1FFG676I PCB Footprint and Symbol

Delivery: FedEx.

TNT ▼EMS.=

Payment: 🥊 PayPal VISA 🧠 📆 🚾 🌃 🕷

XC5VLX50-1FFG676I(Xilinx) Description

More information about the XC5VLX50-1FFG676I: Virtex-5 LX is Highperformance general logic applications. Using the second generation ASMBL (Advanced Silicon Modular Block) column-based architecture, the Virtex-5 family contains five distinct platforms (sub-families), the most choice offered by any FPGA family. Each platform contains a different ratio of features to address the needs of a wide variety of advanced logic designs. In addition to the most advanced, high-performance logic fabric, Virtex-5 FPGAs contain many hard-IP system level blocks, including powerful 36-Kbit block RAM/FIFOs, second generation 25 x 18 DSP slices, SelectIO technology with built-in digitally-controlled impedance, ChipSync source-synchronous interface blocks, system monitor functionality, enhanced clock management tiles with integrated DCM (Digital Clock Managers) and phase-locked-loop (PLL) clock generators, and advanced configuration options. Additional platform dependant features include power-optimized high-speed serial transceiver blocks for enhanced serial connectivity, PCI Express compliant integrated Endpoint blocks, tri-mode Ethernet MACs (Media Access Controllers), and high-performance PowerPC 440 microprocessor embedded blocks. These features allow advanced logic designers to build the highest levels of performance and functionality into their FPGA-based systems. Built on a 65nm state-of-the-art copper process technology, Virtex-5 FPGAs are a programmable alternative to custom ASIC technology. Most advanced system designs require the programmable strength of FPGAs. Virtex-5 FPGAs offer the best solution for addressing the needs of high-performance logic designers, high-performance DSP designers, and high-performance embedded systems designers with unprecedented logic, DSP, hard/soft microprocessor, and connectivity capabilities.

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