












**XC5VLX50-2FFG324C Xilinx Product Details** PDF

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<b>Part Number:</b>	<a href="#">XC5VLX50-2FFG324C</a>
<b>BomEasy Part:</b>	XC5VLX50-2FFG324C-2540744
<b>Category:</b>	<a href="#">FPGA(Field-Programmable Gate Array)</a>
<b>Manufacturer:</b>	<a href="#">Xilinx</a>
<b>Applications:</b>	<input checked="" type="checkbox"/> <a href="#">Communications equipment</a> <input checked="" type="checkbox"/> <a href="#">Enterprise systems</a> <input checked="" type="checkbox"/> <a href="#">Personal electronics</a>
<b>Description:</b>	FPGA Virtex-5 LX Family 46080 Cells 65nm (CMOS) Technology 1V 324-Pin FC-BGA
<b>RoHS:</b>	 Lead free / RoHS Compliant
<b>Stock Category:</b>	In stock
<b>Stock Resource:</b>	Franchised Distributor
<b>Warranty:</b>	1 Year BomEasy Guarantee » <a href="#">Learn More</a>
<b>EDA/CAD Models:</b>	<a href="#">XC5VLX50-2FFG324C</a> PCB Footprint and Symbol
<b>Delivery:</b>	    
<b>Payment:</b>	    

## **XC5VLX50-2FFG324C(Xilinx) Description**

More information about the XC5VLX50-2FFG324C: Virtex-5 LX is High-performance general logic applications. Using the second generation ASMBL (Advanced Silicon Modular Block) column-based architecture, the Virtex-5 family contains five distinct platforms (sub-families), the most choice offered by any FPGA family. Each platform contains a different ratio of features to address the needs of a wide variety of advanced logic designs. In addition to the most advanced, high-performance logic fabric, Virtex-5 FPGAs contain many hard-IP system level blocks, including powerful 36-Kbit block RAM/FIFOs, second generation 25 x 18 DSP slices, SelectIO technology with built-in digitally-controlled impedance, ChipSync source-synchronous interface blocks, system monitor functionality, enhanced clock management tiles with integrated DCM (Digital Clock Managers) and phase-locked-loop (PLL) clock generators, and advanced configuration options. Additional platform dependant features include power-optimized high-speed serial transceiver blocks for enhanced serial connectivity, PCI Express compliant integrated Endpoint blocks, tri-mode Ethernet MACs (Media Access Controllers), and high-performance PowerPC 440 microprocessor embedded blocks. These features allow advanced logic designers to build the highest levels of performance and functionality into their FPGA-based systems. Built on a 65-nm state-of-the-art copper process technology, Virtex-5 FPGAs are a programmable alternative to custom ASIC technology. Most advanced system designs require the programmable strength of FPGAs. Virtex-5 FPGAs offer the best solution for addressing the needs of high-performance logic designers, high-performance DSP designers, and high-performance embedded systems designers with unprecedented logic, DSP, hard/soft microprocessor, and connectivity capabilities.

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